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Serial No.	Filing Date	Examiner	Group Art Unit
<u>10/714,215</u>	<u>November 13, 2003</u>	<u>V. P. Nguyen</u>	<u>2551</u>

Invention:

METHOD AND SYSTEM FOR TESTING MULTI-CHIP INTEGRATED CIRCUIT
MODULES

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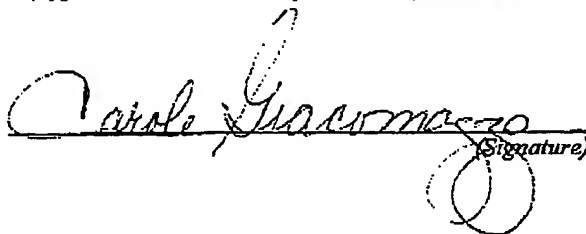
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Patent Number	US 6,984,997 B2
Issue Date	January 10, 2006
Application Number	10/714,215
Filing Date	November 13, 2003
First Named Inventor	Yu
Attorney Docket Number	END820030085US1 (IEN-10-5782)

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